

**AMENDMENTS TO THE CLAIMS**

Please amend the claims as follows.

1. (Currently Amended) A device to load ~~commands~~ of a service ~~[[S]]~~ in a computer system including a server and at least one integrated circuit card ~~[[CARD]]~~ connected together via a network, said at least one integrated circuit card including a first command execution program ~~[[P1]]~~ and a first memory ~~[[M1]]~~, wherein,

~~said at least one integrated circuit card (CARD) includes:~~

said first command execution program ~~[[P1]]~~ controls the integrated circuit of the card into performing a sequence of commands, wherein the sequence includes a command which are that is initially stored in a first memory of the card, and, after executing [[a]] the initially stored command, identifying [[the]] a next command in the sequence of commands to be executed by means of link data stored in the first memory of the card, wherein the link data identifies the next command in the sequence of commands; and wherein said device comprises:

means to search for the next command as identified by the link data in the first memory of the card and, if the next command as identified by the link data is not ~~to be~~ found in the first memory of the card, to ~~search~~ identify on said server for a command sequence ~~block~~ including ~~[[said]]~~ the next command; and

~~and said server includes:~~

means ~~[[ML]]~~ for loading said integrated circuit card with the command sequence identified on said server, if the next command as identified by the link data is not

found in the first memory of the card ~~only a part of a sequence of commands of said service, said part of said sequence of commands of said service comprising at least one said command sequence block (B) of said service (S), wherein upon completion of loading, only said part of said sequence is loaded,~~  
wherein the sequence of commands is associated with the service.

2. (Currently Amended) The device according to claim 1, wherein said first memory (M1) is non volatile.
3. (Currently Amended) The device according to claims 1 or 2, wherein the at least one [[said]] integrated circuit card includes a second non volatile memory (M2) including data specific to ~~at least one~~ the service.
4. (Currently Amended) The device according to claim 1, wherein said server includes means to back up (MSSEQ1, MSSEQ2) ~~at least one~~ the initially stored command sequence block (B) in said first memory (M1).
5. (Currently Amended) The device according to claim 1, wherein said server includes update means (MU) capable of modifying, erasing, and adding, in said first memory (M1), ~~at least one~~ the initially stored command sequence block (B).

6. (Currently Amended) The device according to claim 1, wherein said first memory (~~M1~~) includes a first area (~~Z1~~) and a second area (~~Z2~~), said first area (~~Z1~~) having read and write access by said server and read access by said integrated circuit card, said second area (~~Z2~~) having read and write access by said integrated circuit card.
7. (Currently Amended) The device according to claim 1, wherein said integrated circuit card (~~CARD~~) includes data request means (~~RD~~), wherein data is sent by a service server.
8. (Currently Amended) The device according to claim 1, wherein said integrated circuit card includes means of interpreting (~~MI~~) the initially stored command ~~sequence blocks~~.
9. – 14. (Cancelled)